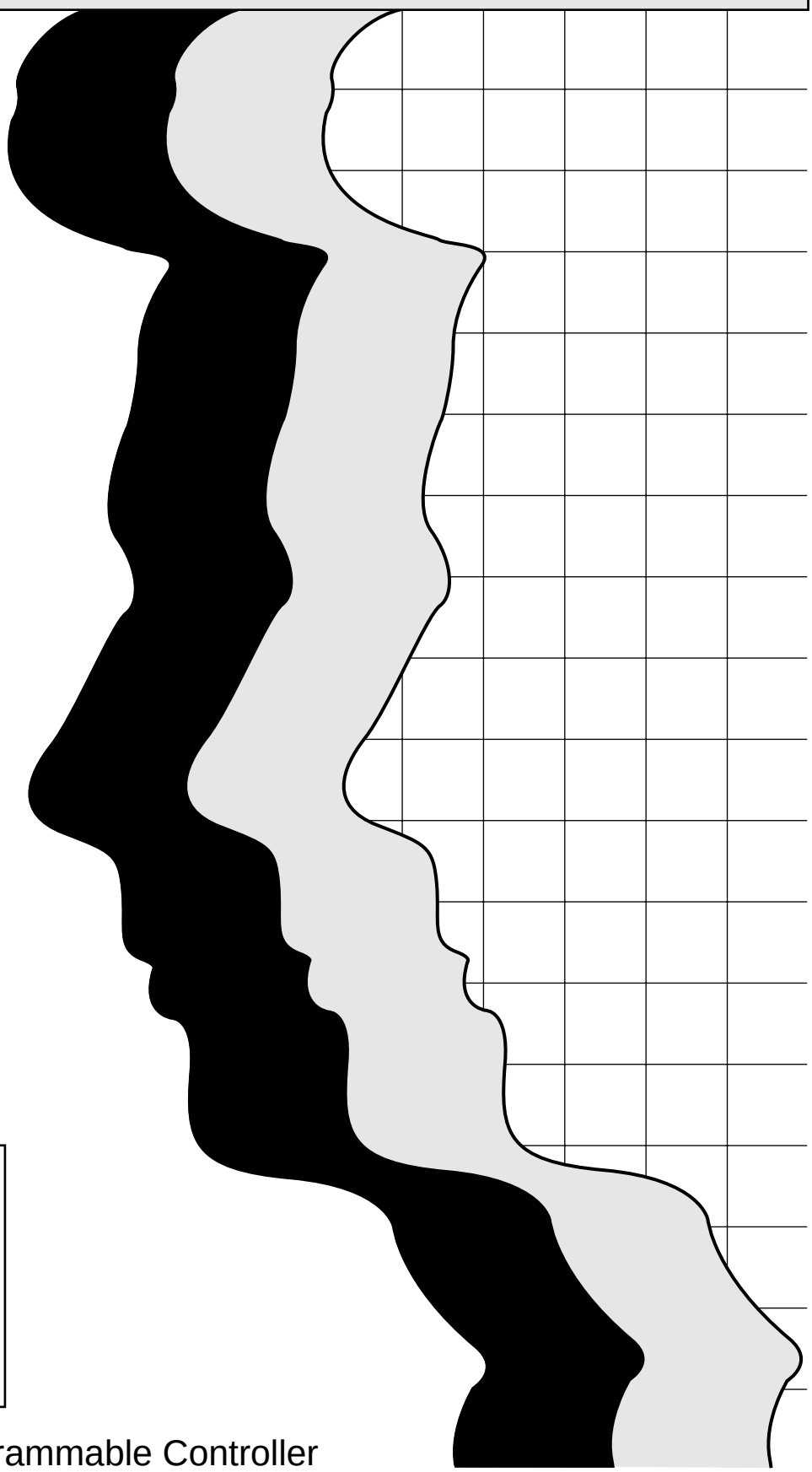
MITSUBISHI

A/D converter module type A1S68AD

User's Manual



MELSEC

Mitsubishi Programmable Controller

● SAFETY PRECAUTIONS ●

(You must read these cautions before using the product)

In connection with the use of this product, in addition to carefully reading both this manual and the related manuals indicated in this manual, it is also essential to pay due attention to safety and handle the product correctly.
The instructions given in this manual are conccrned with this product.
Refer to User's Manual of the CPU module in Use for details on the safety instrutions for the programmable logic controller system.
These SAFETY CAUTIONS are classified into two grades: "DANGER" and "CAUTION"

DANGER	Safety caution given when incorrect handling could result in hazardous situations involving the possibility of death or serious injury.
CAUTION	Safety caution given when incorrect handling could result in hazardous situations involving the possibility of moderate or light injury or damage to property.

Note that, depending on the circumstances, failing to follow a **CAUTION** may also have very serious consequences.
Both of these classes of safety caution are very important and must be observed.
Store this manual carefully in a place where it is accessible for reference whenever necessary, and forward a copy of the manual to the end user.

[Cautions on Design]

⚠ CAUTION

- Do not bundle control lines or communication wires together with main circuit or power lines, or lay them close to these lines.
  As a guide, separate the lines by a distance of at least 100 mm, otherwise malfunctions may occur due to noise.

[Cautions on Mounting]

⚠ CAUTION

- Use the PLC in an environment that conforms to the general specifications in the manual.
  Using the PLC in environments outside the ranges stated in the general specifications will cause electric shock, fire, malfunction, or damage to/deterioration of the product.
- Make sure that the module fixing projection on the base of the module is properly engaged in the module fixing hole in the base unit before mounting the module.
  Failure to mount the module properly will result in malfunction or failure, or in the module falling.

[Cautions on Wiring]

⚠ CAUTION

- Carry out wiring to the PLC correctly, checking the rated voltage and terminal arrangement of the product.
  Using a power supply that does not conform to the rated voltage, or carrying out wiring incorrectly, will cause fire or failure.
- Tighten the terminal screws to the stipulated torque.
  Loose screws will cause short circuits, fire, or malfunctions.
- Make sure that no foreign matter such as chips or wiring offcuts gets inside the module. It will cause fire, failure or malfunction.

[Cautions on Startup and Maintenance]

DANGER

- Do not touch terminals while the power is ON.
  This will cause malfunctions.
- Switch the power off before cleaning or re-tightening terminal screws.
  Carrying out this work while the power is ON will cause failure or malfunction of the module.

CAUTION

- Do not disassemble or modify any module.
  This will cause failure, malfunction, injuries, or fire.
- Switch the power OFF before mounting or removing the module.
  Mounting or removing it with the power ON can cause failure or malfunction of the module.
- Inputting a voltage input in a current input range may cause failure.

[Cautions on Disposal]

CAUTION

- Dispose of this product as industrial waste.

REVISIONS

*The manual number is given on the bottom left of the back cover.

Print Date	*Manual Number	Revision
Sep., 1995	IB (NA) 66576-A	First edition
Apr., 2001	IB (NA) 66576-B	Addition WARRANTY Correction SAFETY PRECAUTIONS, Chapter 2, Section 3.1, 3.2, 3.3, 3.3.1, 3.3.2, 3.3.3, 3.4, 3.7, 3.7.4, 4.1, 4.2, 4.3.2, 5.1, 5.2, 5.3

Japanese Manual Version SH-3537-C

INTRODUCTION

Thank you for choosing the Mitsubishi MELSEC-A Series of General Purpose Programmable Controllers. Please read this manual carefully so that the equipment is used to its optimum. A copy of this manual should be forwarded to the end User.

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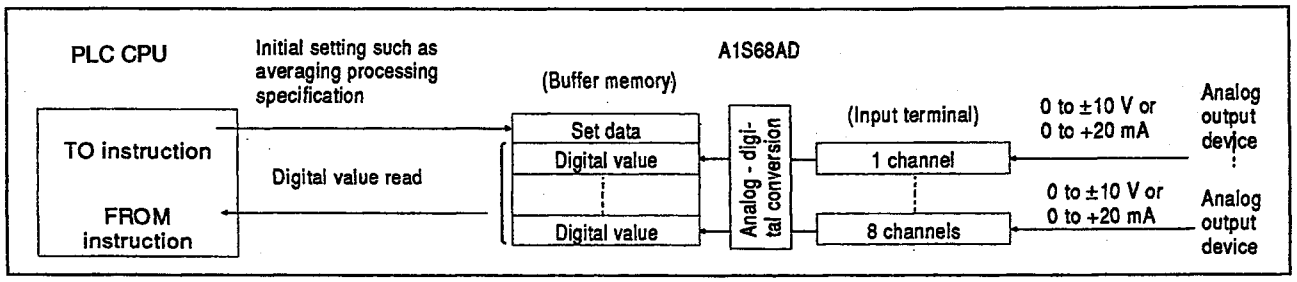
1. INTRODUCTION

This manual describes specifications, handling, programming and other information on the A1S68AD analog to digital converter module (referred to as "A1S68AD") for use with a MELSEC-A series A1SCPU module (hereafter called the PLC CPU).

A1S68AD is used to convert an analog signal (voltage or current input) from an external device of a PLC into a digital value signed 16 bit BIN data.



1.1 Features

A1S68AD has the following features.

(1) Allows analog to digital conversion for 8 channels. A-D conversion of 8 channels is enabled with an A1S68AD.

Voltage input and current input can be selected for each channel by using DIP switches.

(2) Allows selection of the processing method (for each channel).

One of the following processing methods can be selected for each channel:

(a) Sampling processing method whereby digital values are stored one by one in the buffer memory after each A-D conversion.

(b) Averaging processing method whereby A-D conversion is performed the preset number of times or for the preset duration and the mean value of the converted values is stored in the buffer memory as a digital value.

(3) Allows conversion of the enable/disable setting. (Every channel)

The A-D conversion of the enable/disable setting can be set for every channel. The conversion speed can then be shortened by setting an unused channel to conversion disable.

(4) Allows selection of the input range by using DIP switches (for each channel).

The input range can be selected for each channel by using DIP switches. The module has an automatic offset compensation function, making offset/gain adjustment unnecessary.

(5) Allows high-speed A-D conversion.

A-D conversion can be performed at a speed of 0.5 ms per channel.

2. SYSTEM CONFIGURATIONS

(1) Applicable CPU

• A1SCPU(S1)	• A1SJCPU(S3)	• A1SCPUC24-R2	• A2SCPU(S1)
• A2ASCPU(S1/S30)	• A1SHCPU	• A1SJHCPU(S8)	• A2SHCPU(S1)
• A2USHCPU-S1	• A52GCPU	• Q2ASCPU(S1)	• Q2ASHCPU(S1)

(2) Loadable number of A1S68ADs

Any number of A1S68ADs can be loaded provided the number of I/O points of the applicable PLC CPU is not exceeded.

(3) Loading slot

Except in the following cases, an A1S68AD can be loaded in any slot of a base unit.

If an A1S68AD is installed on an extension base unit without a power supply unit (A1S52B(S1), A1S55B(S1) or A1S58B(S1)), the power capacity may be insufficient.

If such installation is unavoidable, select a proper power supply unit, main and extension base units and extension cable, taking the following factors into consideration:

(a) the capacity of the power supply unit on the main base unit,

(b) voltage drop in the main base unit,

(c) voltage drop in the extension base unit, and

(d) voltage drop in the extension cable.

(4) Data link system

The A1S68AD can be loaded at any of the master, local and remote I/O stations of a data link system. For examples of remote I/O station programs, refer to the MELSECNET and MELSECNET/B Data Link System Reference Manuals.

REMARK

For I/O point ranges and calculation of voltage drop, refer to the following manuals:

- A1SJCPU(S3) User's Manual ..... IB (NA) 66446
- A1S/A1SC24-R2/A2SCPU(S1) User's Manual ..... IB (NA) 66320
- A2ASCPU(S1/S30) User's Manual ..... IB (NA) 66455
- A2USHCPU-S1 User's Manual ..... IB (NA) 66789
- A1SJH(S8)/A1SH/A2SHCPU(S1) User's Manual ..... IB (NA) 66779
- Q2AS(H)CPU(S1) User's Manual ..... SH (NA) 3599

3. SPECIFICATIONS

This chapter describes the general specifications, performance specifications, I/O conversion characteristics , and buffer memory spesifications of the A1S68AD.

3.1 General Specifications

Table 3.1 shows the general specifications of the A1S68AD.

Table 3.1 General Specifications

Item	Specifications					
Operating ambient temperature	0 to 55℃					
Storage ambient temperature	-20 to 75℃					
Operating ambient humidity	10 to 90%RH, non-condensing					
Storage ambient humidity	10 to 90%RH, non-condensing					
Vibration resistance	Conforming to *JIS B 3501, IEC 61132-2	———	Frequency	Acceleration	Amplitude	Sweep Count
		When there is Intermittent vibration	10 to 57 Hz	———	0.075 mm	10 times each in X, Y and Z axis (80 minutes)
			57 to 150 Hz	9.8m/S^2	———	
		When there is continuous vibration	10 to 57 Hz	———	0.035 mm	
			57 to 150 Hz	4.9m/S^2	———	
Shock resistance	Conforming to JIS B3501, IEC 61132-2 (147m/S^2, 3 times each in 3 directions)					
Operating environment	No corrosive gas present					
Operating height	2000 m (6562 it) or less					
Installation area	On the control board					
Over-voltage categry *1	II or less					
Pollution rate *2	2 or less					

*1: Indicates the distribution area where the device is assumed to be connected, from the public power distribution network to the local machine device.
Category II is applied to the devices to which the power is supplied from a fixed equipment.
The surge resistace voltage of a rated 300 V device is 2500 V.

*2: This is an index which Indicates the occurrence rate of the conductive object in the anvironment where the device is used.
Pollution rate II indicates that only non-conductive pollution may occur with a possibllity of generating temporary conductivity due to accidental condensation.

3.2 Performance Specifications

Table 3.2 shows the performance specifications of the A1S68AD.

Table 3.2 Performance Specifications

Item	Specifications	
Analog input	Voltage: DC –10 to 0 to +10 V (Input resistance: 1 MΩ Current: 0 to +20 mA (Input resistance: 250 Ω)	Can be selected by setting DIP switches.
Digital output	Signd 16 binary	
I/O characteristics (*1)	Analog input	Digital output
	0 to +10 V -10 to 10 V 0 to 5V or 0 to 20 mA 1 to 5V or 4 to 20 mA	0 to +4000 -2000 to +2000 0 to +4000 0 to +4000
Maximum resolution	Analog input	Digital output
	0 to +10 V -10 to 10V 0 to 5 V 1 to 5 V 0 to 20 mA 4 to 20 mA	2.5 mV 5 mV 1.25 mV 1 mV 5 μA 4 μA
Overall accuracy (accuracy to full scale)	±1.0% (Digital output value ±40)	
Maximum conversion speed	0.5 ms/channel (*2)	
Absolute maximum Input	Voltage: ±35 V Current: ± 30 mA	
Analog input points	8 channels/module	
Insulation method	Photocoupler insulation between input terminals and PC power (No insulation between channels)	
Number of I/O points	Special 32 points	
Connection terminal	20 point terminal block	
External power supply	Not required	
Applicable wire size	0.75 to 1.5 mm^2	
Applicable solderless terminal	R1.25-3, 1.25-YS3, RAV1.25-3, V1.25-YS3A	
Internal current consumption (5 V DC)	0.4 A	
Weight kg	0.27	

*1 The switch is set to the analog input value 0 to +10 V on delivery.

*2 The maximum conversion speed is 1 ms/channel on all channels if averaging processing is set even for only one channel.

POINT

The available analog input range for the overall accuracy is as follows:

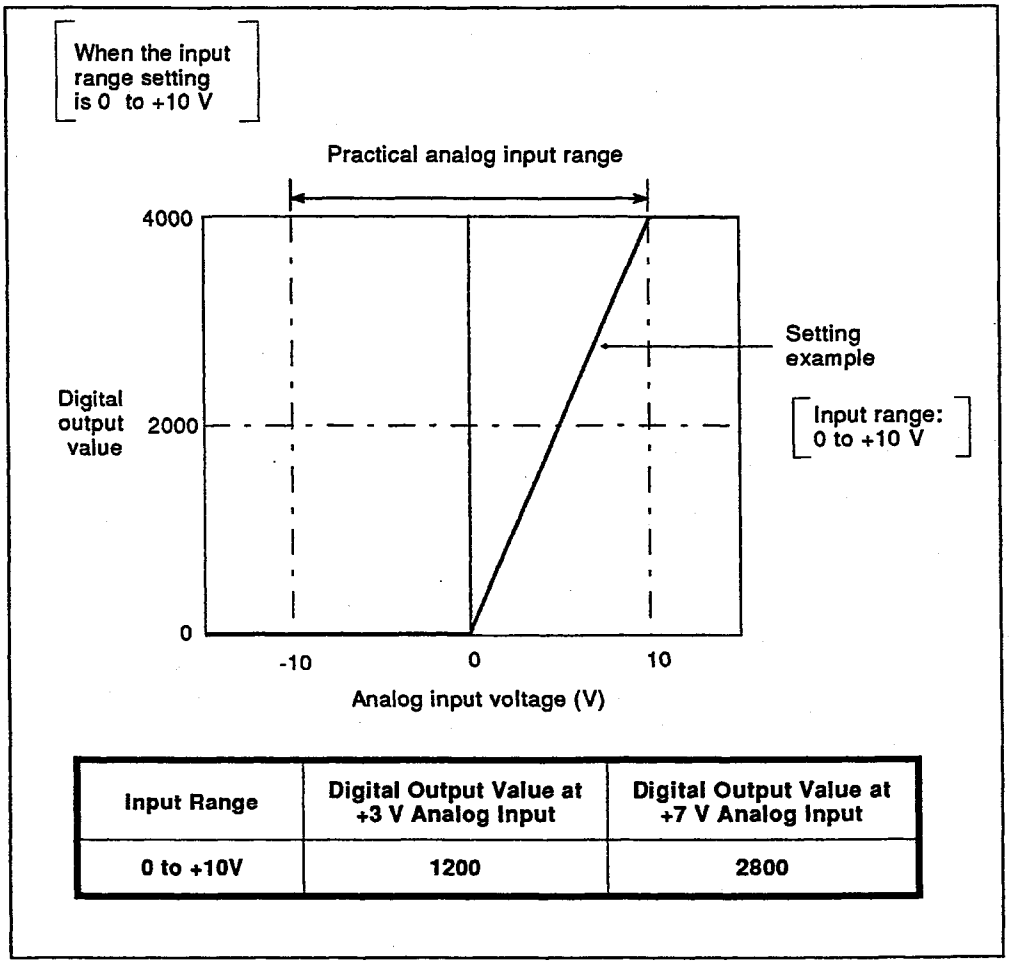
Voltage : –10 to 0 to +10 V
Current : 0 to +20 mA

3.3 I/O Conversion Characteristics

The I/O conversion characteristic is the gradient observed when analog signals (voltage or current input) sent from a source external to the PLC are converted into digital values.

3.3.1 Voltage input characteristics

Figure 3.1 is a graph showing an example of the voltage input characteristic after the input range setting has been changed.



Input Range	Digital Output Value at +3 V Analog Input	Digital Output Value at +7 V Analog Input
0 to +10V	1200	2800

Fig 3.1 Voltage Input Characteristics

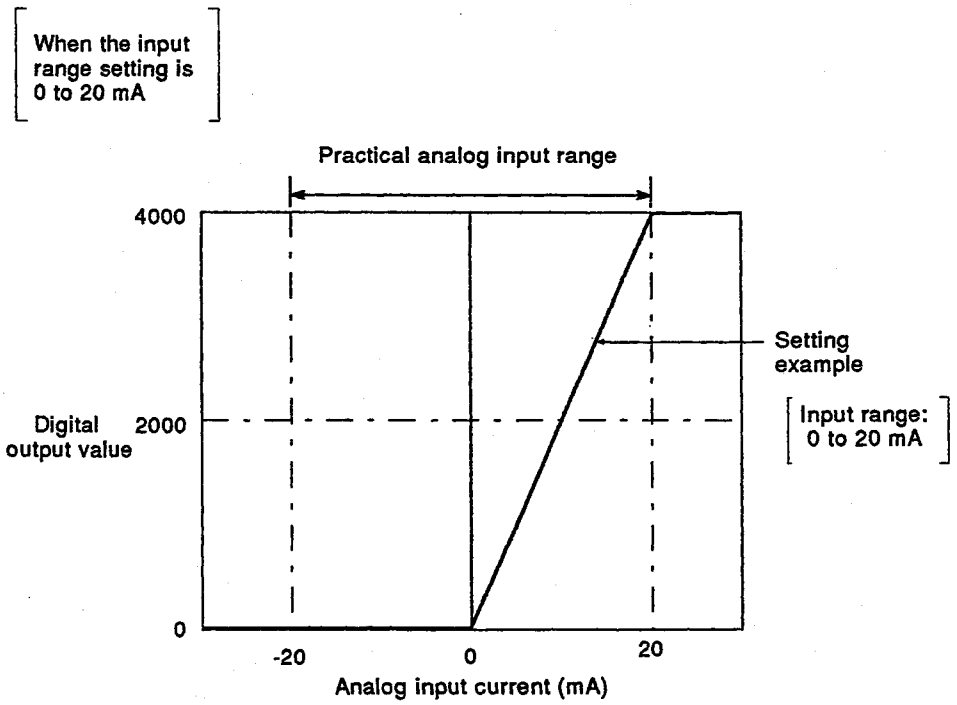
POINTS

(1) Do not input voltage ±35 V or more. Otherwise, elements may be broken.

(2) In the case of an analog input value which, on conversion, will generate a digital output value exceeding the maximum (2000/4000) or minimum (-2000/0) digital output value set for the input range, the digital output value is fixed at the maximum (2000/4000) or minimum (-2000/0) digital value set for the input range.

3.3.2 Current input characteristics

Figure 3.2 is a graph showing an example of the voltage input characteristic after the input range setting has been changed.



Input Range	Digital Output Value at 5 mA Analog Input	Digital Output Value at 10 mA Analog Input
0to20mA	1000	2000

Fig 3.2 Volage Input Characteristics

POINTS

(1) Do not input a voltage ±30 mA or more. Otherwise, a high temperature will occur and cause a problem.

(2) In the case of an analog input value which, on conversion, will generate a digital output value exceeding the maximum (4000) or minimum (0) digital output value set for the input range, the digital output value is fixed at the maximum (4000) or minimum (0) digital value set for the input range.

3.3.3 Overall accuracy

The overall accuracy is the accuracy with respect to the full-scale digital output value.

Even if the input range setting is changed and the I/O characteristic varies accordingly, the overall accuracy does not change, and remains within the range specified in the Performance Specifications table.

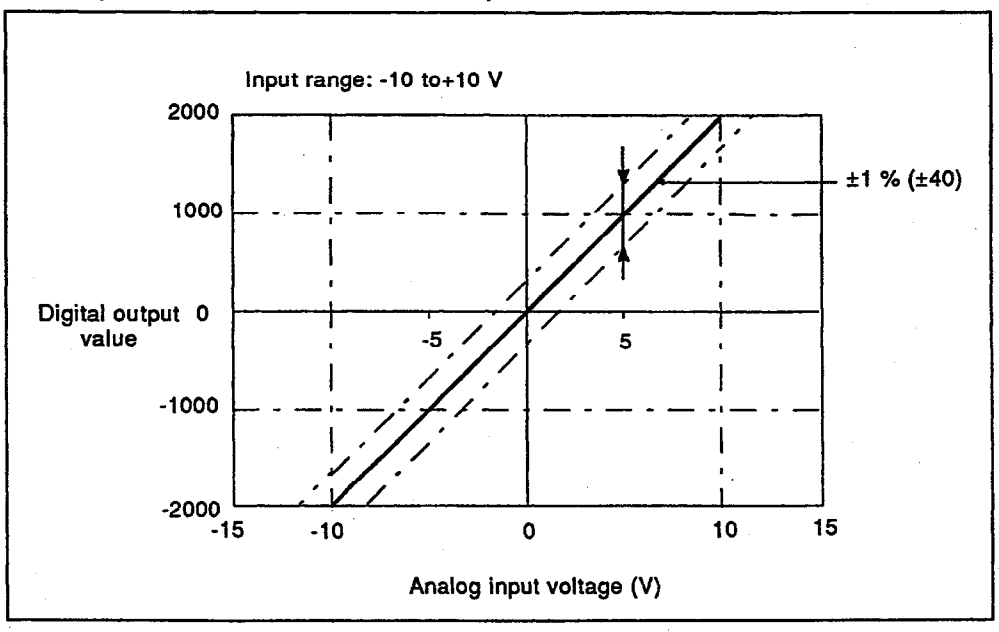


Fig 3.3 Overall Accuracy of the Voltage Input Characteristics

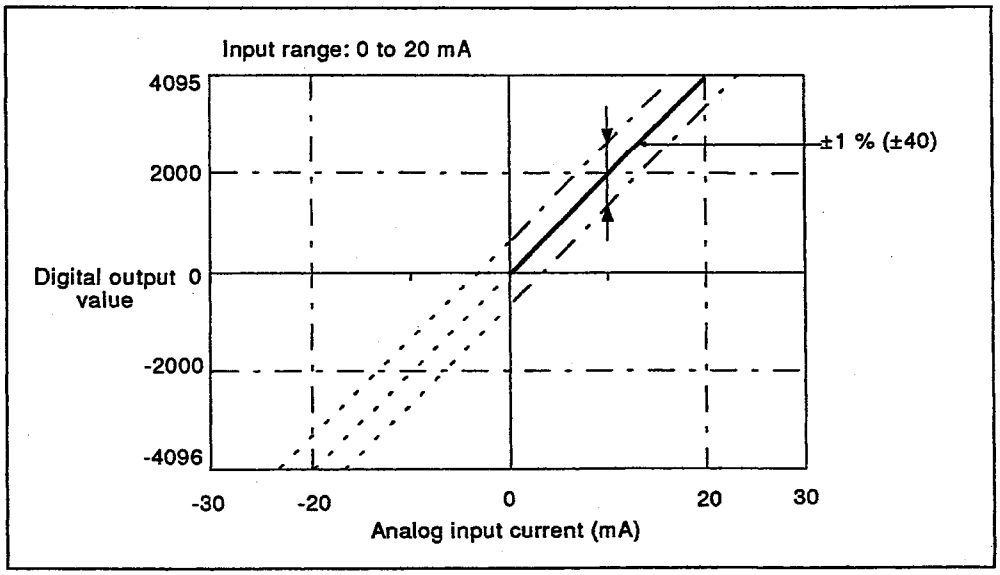


Fig 3.4 Overall Accuracy of the Current Input Characteristics

3.4 Functions List

Table 3.3 shows the functions of A1S68AD.

Table 3.3 A1S68AD Functions List

Item	Description	Reference
A-D conversion-enable/ disable setting	• A-D conversion enable/disable is specified for each channel. (Default: All channels are set to "enable".) • Sampling time can be shortened by switching the unused channel to "disable".	Section 3.7.2
Input range setting	• The input range can be set for each channel, and the I/O conversion characteristic can be changed.	Section 3.3 Section 4.2
Averaging process specification	• A-D conversion is done for the each channel with a setting count or setting time. The averaging process is done for the A-D conversion data, and the average is set to the buffer memory as the digital output value.	Section 3.7.3
Sampling process specification	• Analog input value is converted to the digital value for each channel, and is stored in the buffer memory as the digital output value.	Section 3.7.3

3.5 Maximum Conversion Speed

Conversion speed is the time it takes when a digital value is written to the buffer memory after the A-D conversion by switching channel.

The following explains the maximum conversion speed.

3.5.1 Conversion speed for one channel

Conversion speed for one channel of A1S68AD is 0.5 ms.

When several channels are used, "0.5 ms x the number of conversion-enabled channels" equals the sampling time.

The conversion speed is 1 ms/channel on all channels if averaging processing is selected even for only one channel.

3.5.2 Influence on the maximum conversion speed by executing the FROM/TO instruction

When the FROM/TO instruction is not executed, the maximum conversion speed is as mentioned above. Executing the FROM/TO instruction influences the maximum conversion speed as follows.

(1) If the time it takes to write a digital value converted from an analog value overlaps with the execution of FROM/TO processing, writing to the buffer memory is delayed until the FROM/TO processing is completed.

(2) If the time it takes for the switching channel overlaps with the execution of FROM/TO processing, the switching channel is delayed until the FROM/TO processing is completed.

(3) When a digital value that has been converted from an analog value is being written to the buffer memory with the switching channel, the FROM/TO processing is delayed until the writing or switching channel is completed.

(4) Designate the FROM/TO instruction to read and write a lot of data at one time. The smaller the number of FROM/TO instructions, the less the maximum conversion speed is influenced.

3.6 CPU I/O Signals

This section explains the allocation of the I/O signals and the function of each signal.

3.6.1 Overview of I/O signals

The A1S68AD uses 32 signal points for input and output.

Table 3.4 gives the allocation and description of the I/O signals.

X devices refer to the input signals from the A1S68AD to the PLC CPU.

Y devices refer to output signals from the PLC CPU to the A1S68AD.

The device numbers (input signals) shown in the table are used when the A1S68AD is loaded into slot 0 of the main base unit.

Table 3.4 I/O Signals

Signal Direction: A1S68AD →PC CPU		Signal Direction: PC CPU → A1S68AD	
Device No.	Signal Description	Device No.	Signal Description
X0	WDT error flag	Y0 to Y11	Unusable
X1	A-D conversion READY		
X2	Error flag	Y12	Error reset
X3 to X1F	Unusable	Y13 to Y1F	Unusable

POINT

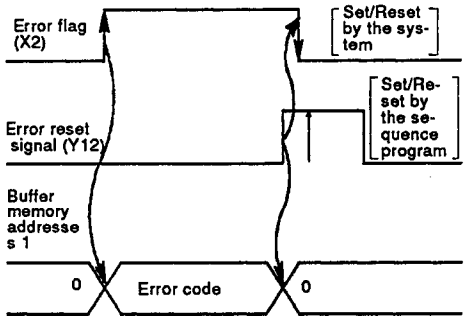
If any of the devices (Y0 to Y11, and Y13 to Y1F) are used (turned ON/OFF) in the sequence program, the functions of the A1S68AD cannot be guaranteed.

If any of the devices Y0 to Y1F has the same number as any of the devices X0 to X1F, then the device cannot be used as an internal relay.

3.6.2 I/O signal functions

The following table explains the function of each I/O signal of A1S68AD.

Table 3.5 I/O Signal Functions

Device No.	Signal	Description	Remark
X0	WDT (watch-dog timer) error flag	This flag is set when the self-diagnosis function of the A1S68AD detects a WDT error.	• While the error flag is set, the A-D conversion of the A1S68AD will not RUN. • If the error flag (X0) is set, hardware malfunction may occur.
X1	A-D conversion READY	• This signal is turned ON when the A/D conversion is ready after turning on or resetting the CPU . • The A/D conversion READY signal (X1) can also be used as the buffer memory read/write interlock.	• A-D conversion READY indicates the point in time when A-D conversion has been completed once on all channels for which A-D conversion is enabled and the digital output values have been stored in the buffer memory.
X2	Error flag	• This flag is set when an error other than the watch dog timer error occurs in the A1S68AD. • If the error reset signal is set, the error code is stored in the error code storage area. • If the error reset signal is turned ON, this error flag is reset.	
Y12	Error reset	• Turning ON the error reset signal resets the error flag, and clears the check code of the buffer memory. It is replaced by "0". • The RUN LED on the front of the module is switched to ON (normal operation) from flashing (error).	

3.7 Buffer Memory

The A1S68AD has a buffer memory (not battery backed) for data communication with the CPU.

The buffer memory assignment and data maps are indicated below.

3.7.1 Buffer memory assignment

The following figure shows the buffer memory assignment.

Address(Decimal)		Default Value	Read	Write	Reference
0	A-D conversion enable/disable	00FFH(All channels are set to set to"ennable".)	Enabled	Enabled	Section3.7.2
1	Writing data error code	0(All channels)	Enabled	Disabled	Section3.7.5
2	Average processing specification	0(sampling processing specified for all channels)	Enabled	Enabled	Section3.7.3
3 to 9	Unusable		Disabled	Disabled	
10	CH1 Average time, count	0	Enabled	Enabled	Section3.7.3
11	CH2 Average time, count				
12	CH3 Average time, count				
13	CH4 Average time, count				
14	CH5 Average time, count				
15	CH6 Average time, count				
16	CH7 Average time, count				
17	CH8 Average time, count				
18 19	Unusable		Disabled	Disabled	
20	CH1 Average time, count	0	Enabled	Disabled	Section3.7.4
21	CH2 Average time, count				
22	CH3 Average time, count				
23	CH4 Average time, count				
24	CH5 Average time, count				
25	CH6 Average time, count				
26	CH7 Average time, count				
27	CH8 Average time, count				
28	A-D conversion completed flag	0	Enabled	Enabled	Section3.7.6
29	Unusable		Disabled	Disabled	

Fig.3.6 Buffer Memory Assignment

POINT

Do not write values from the PLC CPU to any write enabled area in the buffer memory. It will result in an error unless the written values are identical. The "RUN" LED of the A1S68AD will flash, the error flag (X2) will be set, and an error code, which indicates that a value has been written to a write enabled area, will be stored in the error code area in the buffer mamory.

In such a case, the A1S68AD will write the original data over the written data and continue processing.

3.7.2 Setting of A-D conversion-enable/disable

A-D conversion-enable/disable for each channel of A1S68AD can be set at address 0 of the buffer memory by writing "1" (enable) or "0" (disable) by the channel.

The sampling period can be shortened by setting an unused channel to conversion disable.

(Default: All channels A-D conversion enabled)

Example:

Sampling period when setting only channels 1 and 3 to A-D conversion-enable

2 (Number of enable channels) x 0.5 ms (Conversion speed of one channel) = 1 ms

(1) Method of setting the conversion-enable/disable
Set the conversion-enable/disable for every channel.

	b15 to b8	b7	b6	b5	b4	b3	b2	b1	b0
Address 0	——	CH8	CH7	CH6	CH5	CH4	CH3	CH2	CH1
	Ignored.	Specify channels							

1 : A-D conversion enable
0 : A-D conversion disable

(2) Processing in the A1S68AD when setting conversion enabled/disabled

(a) Processing when the A-D conversion setting is switched from disabled to enabled

After the A-D conversion READY flag (X1) has been reset, sampling of the enabled channels begins. The A-D conversion READY flag and the A-D conversion completed flag for the corresponding channels are set as soon as one cycle of sampling processing/averaging processing is completed.

Valid digital output values are stored in the buffer memory just before these flags are set.

(b) Processing when the A-D conversion setting is switched from enabled to disabled

When averaging processing is specified, all data within the work area for averaging processing is initialized.

This means that processing will start from the first sampling when conversion enabled is specified for the next averaging processing.

Although the A-D conversion completed flag for the corresponding channels is reset immediately, the A-D conversion READY flag remains unchanged.

The buffer memory retains the digital output values before conversion disabled was specified.

3.7.3 Setting of the sampling process/averaging process

(1) Method of outputting a digital value of the sampling process and the averaging process

(a) Sampling process

An analog input value is converted to a digital value, and the digital output value is stored in a buffer memory.

The storage time for a digital output value that the sampling process was executed on in the buffer memory depends on the number of A-D conversion-enabled channels.

Processing time =
(number of A-D conversion-enabled channels) x 0.5 (ms)
Maximum conversion speed ↑

Example)
When channels 1, 2 and 3 are set to conversion-enable:
3 x 0.5 = 1.5 (ms)

(b) Averaging process

A1S68AD executes the A-D conversion for a channel that was specified for averaging process by a PLC CPU for the set number of times of set time. The total values except a maximum value and a minimum value are averaged and the stored in the buffer memory.

However, when the processing number of times is no more that two, the sampling process is executed. Data for averaging processing is initialized when the A-D conversion setting is switched from enabled to disabled.

1) When the averaging process by time is specified

The setting time is set in 1ms unit .

The number of processing events within the preset time de-pends on the number of channels for which A-D conversion is enabled.

Processing time =

$$\frac{\text{Setting time}}{\text{(number of A-D conversion-enabled channels)} \times \frac{1\ (\text{ms})}{\text{Maximum conversion speed} \uparrow}}$$

Example)
When the number of times of A-D conversion-enabled channels is 4 and the setting time is 8000 msec

8000 ÷ (4 x 1) = 2000 (time)

2) When the number of times for the averaging process is specified

The time in which the average is stored to the buffer memory follows the number of A-D conversion-enabled channels.

Processing time = (the setting number of times) x (A-D conversion-enabled channel) x 1 (ms)

Maximum conversion speed

Example)
When example channels 1, 2, 3 and 4 are set to A-D conversion-enable, and the setting number of times is set to 50 times

50 x 4 x 1 = 200 (ms)

(2) Specifying the averaging process and selecting time/number of times

(a) When the power supply is turned ON, and the A1S68AD A-D conversion READY signal turns ON, all of the channels are specified for the sampling process.

(b) When sampling processing or averaging processing is specified, and to perform averaging processing, specify the average time or average count.

	b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
Address 2	CH8	CH7	CH6	CH5	CH4	CH3	CH2	CH1	CH8	CH7	CH6	CH5	CH4	CH3	CH2	CH1

Specify the channel in which the averaging process is to be executed.
1: Averaging prosess
0: Sampling process

Specify the time/number of times.
1: Time averaging process
0: Number of times

POINTS

(1) When the averaging process is specified, the number of times or time for the averaging process should be set beforehand.

(2) When the averaging process is not specified, despite the specification of the time/number of times, sample processing will be executed.

(3) Specifying the average time and the average number of times

(a) Average time or the average number of times is written in the address that corresponds to the channel of the buffer memory addresses 10 to 17 for every channel that the averaging process was specified.

Average time when a power supply is turned ON and the average number of times is set to 0.

(b) The allowable setting range is as follows.

Averaging process specifying the number of times: 1 to 20000 times

Averaging process by specifying time: 4 to 10000 ms

POINT

When a value is set and written outside the above range, a setting error occurs, and the buffer memory is not rewritten.

The A1S68AD then completes the A-D conversion processing according to the average time and the number of times before a setting error occurs.

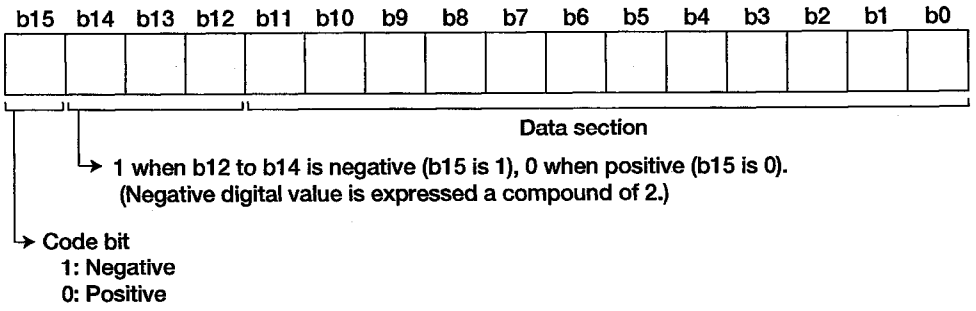
3.7.4 Digital output value

(1) Digitally converted values are stored for each channel in buffer memory addresses 20 to 27.

The table below shows the digital output values for each input range:

Input Range	Resolution	Digital Output Value
0 to 10 V	2.5 mV	0 to 4000
-10 to 10 V	5.0 mV	-2000 to 2000
0 to 5 V	1.25 mV	0 to 4000
1 to 5 V	1.0 mV	0 to 4000
0 to 20 mA	5.0 μA	0 to 4000
4 to 20 mA	4.0 μA	0 to 4000

(2) The digital output value (-2000 to 4000) is expressed by 16-bit coded binary.


b15 b14 b13 b12 b11 b10 b9 b8 b7 b6 b5 b4 b3 b2 b1 b0
Data section
1 when b12 to b14 is negative (b15 is 1), 0 when positive (b15 is 0).
(Negative digital value is expressed a compound of 2.)
Code bit
1: Negative
0: Positive

3.7.5 Writing data error code

(1) The A1S68AD checks the preset range of the average time of count written from the PLC CPU, and whether data has been written to a write disabled area.

If the setting is out of the range or data is written to a write disabled area, the error flag (X2) will be set, and the corresponding error code will be stored as a 16-bit binary value at buffer memory address 1.

In such a case, the A1S68AD will write the original data over the written data and continue processing. For details of error codes, refer to Section 6.1.

(2) When several errors occur, the error code that occurred at first is stored in A1S68AD, and any errors after that are not stored.

(3) The error code is reset by turning ON Y12 with a sequence program.

(4) When the error is reset, the error flag (X2) will be reset, and the error code stored in the buffer memory will be cleared to 0. At the same time, the "RUN" LED will stop flashing and come on.

3.7.6 A-D conversion completed flag

(1) When after turning ON a power supply and an A-D conversion READY signal (X1) turns ON, channels 1 to 8 have already completed all of the A-D conversion.Therefore, 00FF$_H$ is stored in the buffer memory.

(2) Only when the A-D conversion-enable/disable setting (address 0) is changed, the A-D conversion completed flag processing after turning on a power supply is done one time.

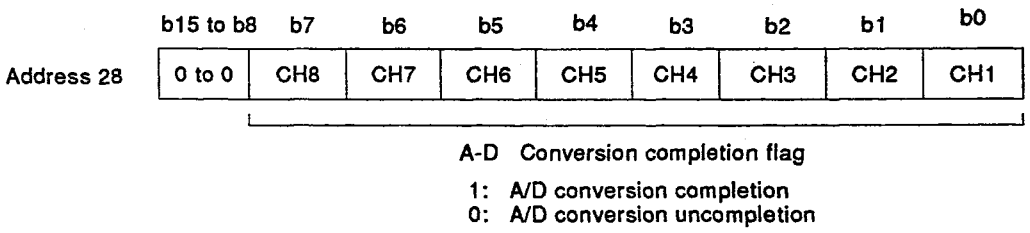
- When A-D conversion-disable is switched to enable

  When the averaging processing is specified, the averaging process for the average number of times or average time is completed, and a flag is set to 1 after storing a digital value translated from an analog value in the buffer memory.

- When A-D conversion-enable is switched to disable

  The A-D conversion completed flag of a corresponding channel is set to 0.

(3) There is an A-D conversion-completed flag classified by a channel.



(4) An A-D conversion-completed flag can be used as an interlock to read a digital value of a channel that the averaging process is done.

4. PRE-OPERATION SETTINGS AND PROCEDURES

4.1 Handling Instructions

The following explains the handling instructions for the A1S68AD.

(1) Since a case of the AIS68AD and its terminal block are made of resin, do not let the module fall or give a strong.

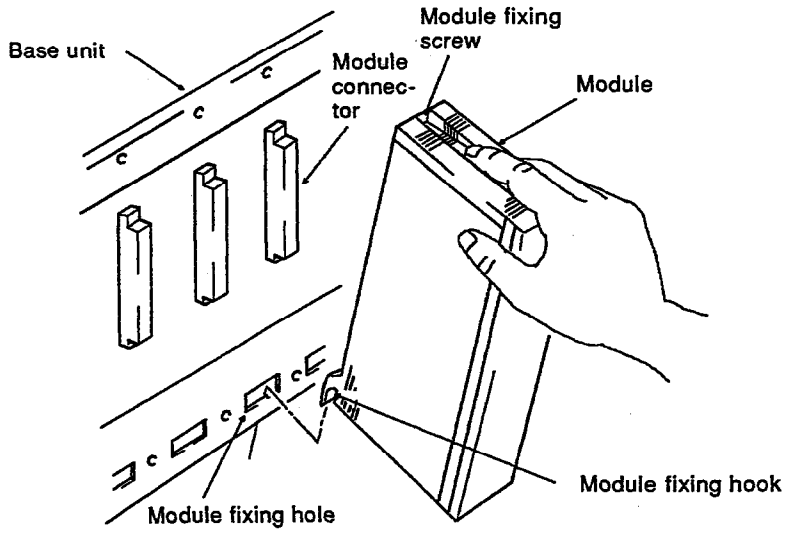
(2) Do not remove the printed circuit boards from their housing. Otherwise, it may cause failnre.

(3) Make sure that no conductive debris can enter the module. Make sure that any debris is removed from the module. Guard particularly against cut off wires.

(4) Tighten the terminal screws and mounting screws as specified below:

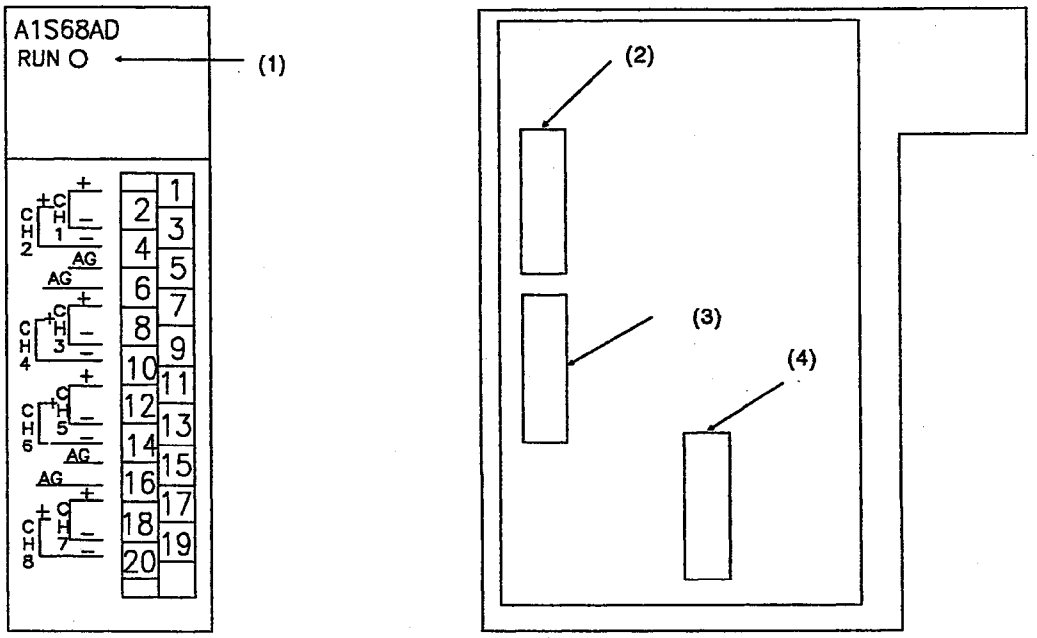
Screw	Tightening Torque Range N·cm
Module fixing screw (M4 screw)	78 to 118
Terminal block installation screw (M3.5 screw)	59 to 88
Terminal block installation screw (M4 screw)	78 to 118

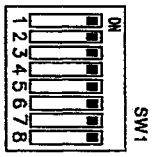
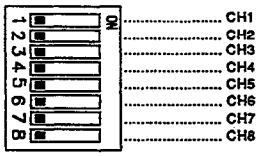
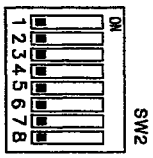
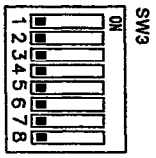
(5) To install a module to the base unit, insert the module fixing hook into the module fixing hole on the base unit and tighten the module fixing screw. To remove a module, loosen the module fixing screw, and pull the module fixing hook out of the module fixing hole.



4.2 Names and settings of each section

The following gives the Names and settings of each section for each part of the A1S68AD



No.	Name and Appearance	Description
(1)	RUN LED RUN ◌	Displays the operation status of the A1S68AD. On : Normal operation Flash : Write disabled error or average time/count setting error Off : 5 V power cut or watchdog timer error
(2)	Input range selector switch SW1 	Used to set the input range for each channel. The DIP switch numbers 1 to 8 correspond to the channel numbers. SW1 to SW3 
(3)	Input range selector switch SW2 	Setting of CH1
(4)	Input range selector switch SW3 	

Input Range	SW 1-1	SW 2-1	SW 3-1
-10 to +10V	OFF	OFF	OFF
0 to 10 V	ON		
0 to 5 V	OFF	ON	
1 to 5 V	ON		
0 to 20mA	OFF	ON	ON
4 to 20mA	ON		
Setting disabled	OFF	OFF	
Setting disabled	ON		

Set CH2 to CH8 in the same manner.

Caution: Applying a voltage input when a current input range is selected may cause a failure.

4.3 Wiring

This section explanins the wiring instrutions and gives a connection example of a module.

4.3.1 Wiring instructions

Protect the external wiring against noise (extraneous signals) with the following precautions:

(1) Separate AC and A1S68AD external input signal wiring that is not affected by a surge of alternating current and induction.

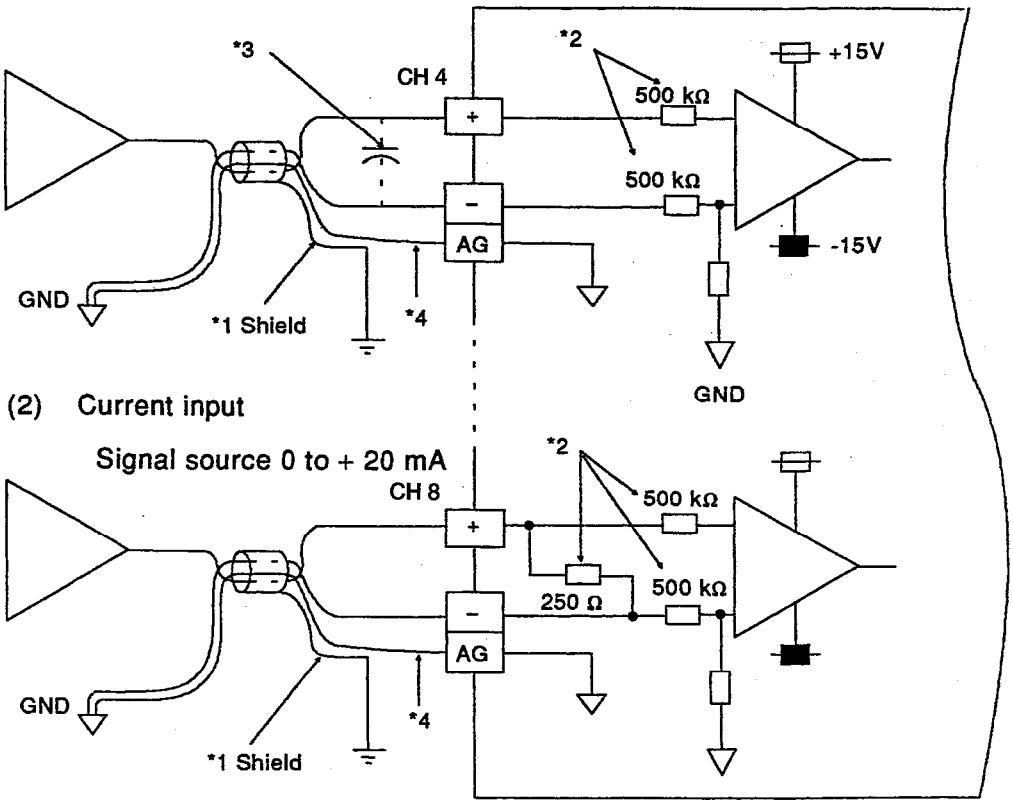
(2) Keep the external wiring at least 10 cm away from the main circuit, high-voltage wires and load carrying wires not originating from the PLC. Otherwise, the external wiring will be affected by noise, surge or induction.

(3) Where applicable, ground the shielding of all the wires to a common ground point.

4.3.2 Connection example of module

(1) Voltage input

Signal source 0 to ± 10 V



(2) Current input

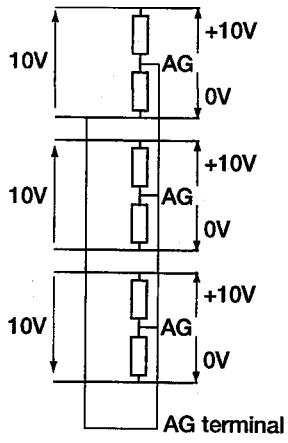
Signal source 0 to + 20 mA

*1: Use two-core shielded wiring (twisted)

*2: Indicates the input resistance of the A1S68AD. (For voltage input, turn off the 250 Ω resistor with the DIP switch.)

*3: If either noise or ripple is generated by the external wiring, connect a 0.1 to 0.47 μF (25V or more voltage resistance parts) capacitor to the input terminal of the external device.

*4: AG is the GND terminal of the analog circuit. Connecting it to the GND terminal of an external device is not mandatory, but a higher level of accuracy may be obtained if it is connected.



There is (±10V input range for 3 channels or more and when the common for the external equipment that is connected to these channels is shared, always connect the AG terminal and the shared common for the external equipment. (Refer to figure below)

4.4 Check and Maintenance

For ideal running conditions, follow the check items described in the CPU module User's Manual.

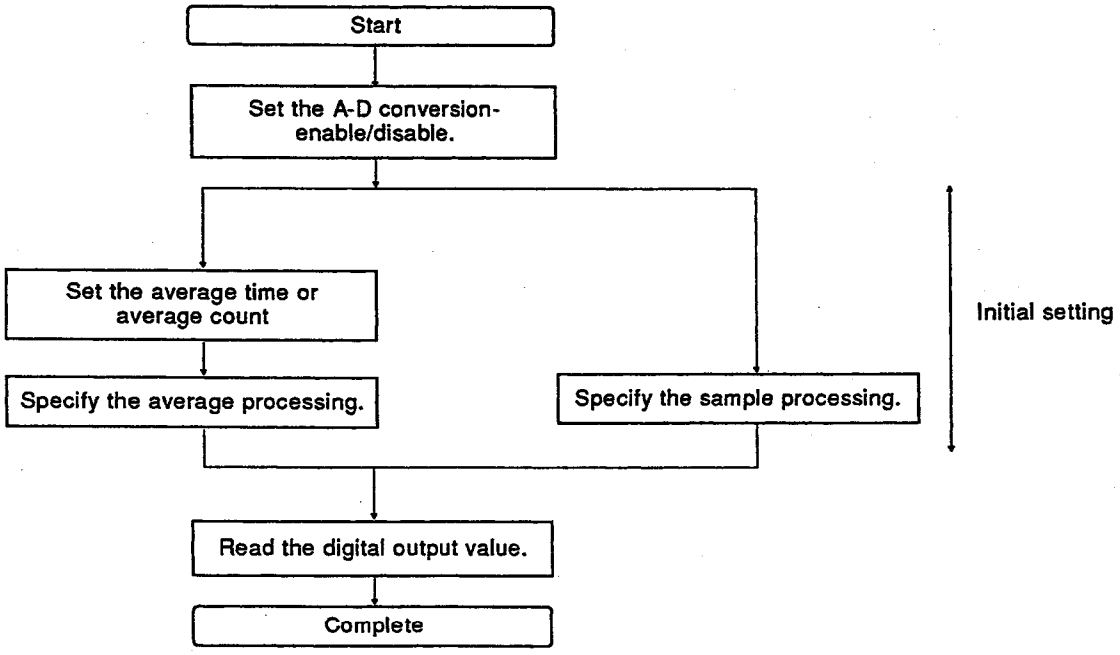
5. PROGRAMMING

This section explains the programming procedure, a basic program, and a read/write programming example when using the A1S68AD.

See to Section 3.7 for the buffer memory and the ACPU Programming Manual for the programming instruction details.

5.1 Programming Procedure

When creating a program to execute the A-D conversion of A1S68AD use the following procedure.



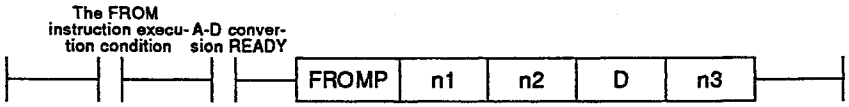
Point

During each of the processes of the special function module, access from the PLC CPU will have priority.
Accordingly, if frequent access to the buffer memory of the special function module made from the PLC CPU, it will not only extend the scan time of the PLC CPU, delays in each of the processes of the special function module will occur.
Only use the FROM/TO and other such commands to access the buffer memory from the PLC CPU when necessary.

5.2 Basic Program for Read/Write

(1) Read from A1S68AD ... FROM, FROMP, DFRO, and DFROP instruction

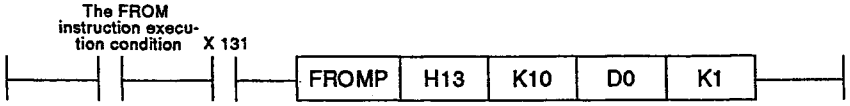
Format



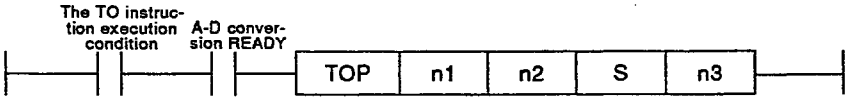
Symbol	Description	Device that Can be Used
n1	The first two (2) digits when the head I/O number is allocated to A1S68AD in three (3) digits of hexadecimal	K, H
n2	Head address of the buffer memory in which data is stored	K, H
D	Head number of the device in which data is stored	T, C, D, W, R
n3	Number of words of read data	K, H

Example

When the A1S68AD is allocated to I/O X130 to 14F and the Y130 to 14F, the data is read from address 10 of the buffer memory to one (1) word D0.



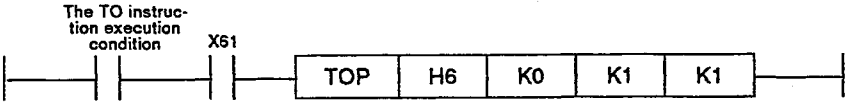
(2) Writing to A1S68AD ... TO, TOP, DTO, and DTOP instruction



Format

Symbol	Description	Device that Can be Used
n1	The first two (2) digits when the head I/O number is allocated to A1S68AD in three (3) digits of hexadecimal	K, H
n2	Head address of the buffer memory in which data is stored	K, H
S	Number or constant of a head device in which writing data is stored	T, C, D, W, R, K, H
n3	Number of words of write data	K, H

Example



When A1S68AD is allocated to I/O X60 to 7F and Y60 to 7F, 1 is written in address 0 of a buffer memory

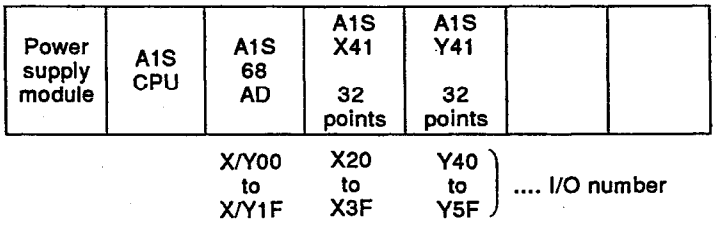
5.3 Initial Setting Program and Example of Digital Output Value Read Program

This programming example is used to read the digital output value converted from an analog value with the condition that channels 1 to 3 are used.

Channel 1 is for the sample processing, channel 2 is for the average processing per 50 times, and channel 3 is for the average processing per 1000 ms. When a writing error occurs, an error code is displayed in BCD.

Sample condition of a program

(1) System configuration



(2) Initial setting

(a) A/D conversion-enabled channel ..... 1, 2 and 3 channels

(b) Average processing channel by count Channel 2, count setting: .....50 times

(c) Average processing channel by time Channel 3, time setting: .....1000 ms

(3) Device to be used by user

(a) Writing data error reset signal X20

(b) Digital output value read command input signal X21

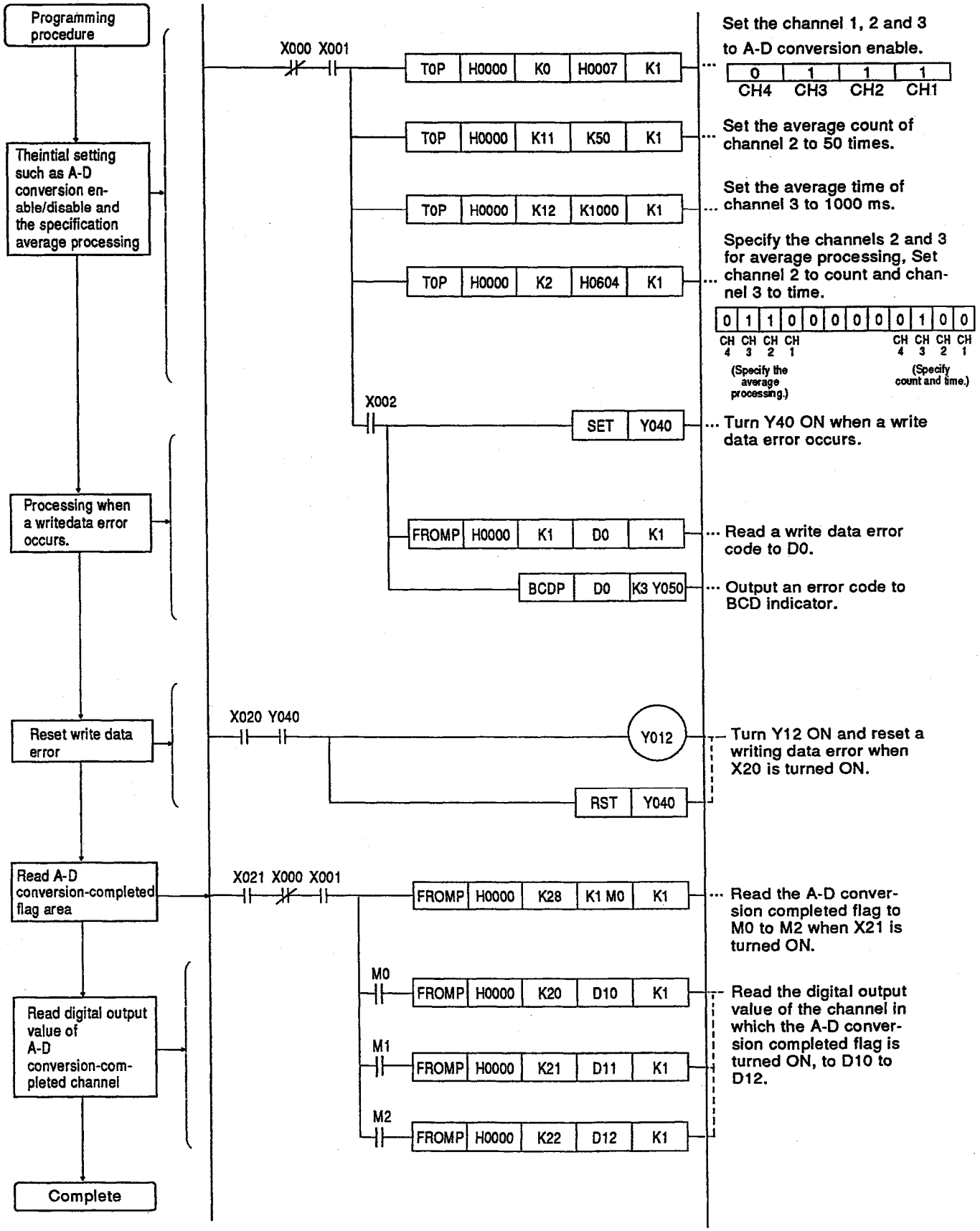
(c) Writing data error occurrence display Y40

(d) Writing data error code BCD output ..... Y50 to 5B

(e) Writing data error code storage data register D0

(f) A-D conversion completed flag storage device ..... M0 to M2

(g) Digital output value read data register .....D10 to D12


Programming procedure
Theintial setting such as A-D conversion enable/disable and the specification average processing
Processing when a writedata error occurs.
Reset write data error
Read A-D conversion-completed flag area
Read digital output value of A-D conversion-completed channel
Complete
X000 X001
TOP H0000 K0 H0007 K1
Set the channel 1, 2 and 3 to A-D conversion enable.
0 1 1 1
CH4 CH3 CH2 CH1
TOP H0000 K11 K50 K1
Set the average count of channel 2 to 50 times.
TOP H0000 K12 K1000 K1
Set the average time of channel 3 to 1000 ms.
TOP H0000 K2 H0604 K1
Specify the channels 2 and 3 for average processing, Set channel 2 to count and channel 3 to time.
0 1 1 0 0 0 0 0 0 1 0 0
CH 4 CH 3 CH 2 CH 1
CH 4 CH 3 CH 2 CH 1
(Specify the average processing.)
(Specify count and time.)
X002
SET Y040
Turn Y40 ON when a write data error occurs.
FROMP H0000 K1 D0 K1
Read a write data error code to D0.
BCDP D0 K3 Y050
Output an error code to BCD indicator.
X020 Y040
Y012
Turn Y12 ON and reset a writing data error when X20 is turned ON.
RST Y040
X021 X000 X001
FROMP H0000 K28 K1 M0 K1
Read the A-D conversion completed flag to M0 to M2 when X21 is turned ON.
M0
FROMP H0000 K20 D10 K1
M1
FROMP H0000 K21 D11 K1
M2
FROMP H0000 K22 D12 K1
Read the digital output value of the channel in which the A-D conversion completed flag is turned ON, to D10 to D12.

6. TROUBLESHOOTING

Assorted problem conditions and a troubleshooting guide for the A1S68AD are described below.

6.1 Error Code List

When data is written to an A1S68AD from a PLC CPU or an error occurs by reading data (RUN LED of A1S68AD flashes), the following error codes are stored in address 1of the A1S68AD buffer memory.

Table 6.1 Error Code List (Detected with A1S68AD)

Error Code	Cause	Corrective Action
102	Data was written to the read-only area.	Modify the specified place for the read-only area.
1[ ] 0	• A value outside 4 to 10000 ms was set for the average time. • [ ] indicates the channel No. in which an error occurred.	Set the average time within 4 to 10000 ms.
1[ ] 5	• A value outside 1 to 20000 times was set for the average count. • [ ] indicates the channel No. in which an error occurred.	Set the average count within 1 to 20000 times.

(1) When several errors occur, the error code that occurred first is stored, and any errors after that are not stored.

(2) The error code is reset by turning ON Y12 with a sequence program. (See Section 3.6.)

6.2 Troubleshooting

The following explains the simple troubleshooting for A1S68AD. Refer to the CPU Module User's Manual for a PLC CPU module.

6.2.1 When the RUN LED of A1S68AD Flashes

Items to Check	Corrective Actions
Is the data which cannot be executed read/write written to A1S68AD?	Confirm the error cause with the error code list in Section 6.1, and correct the sequence program.

6.2.2 When the RUN LED of A1S68AD goes OFF

Items to Check	Corrective Actions
Is X0 (WDT error) set?	Reset the PLC CPU. If the RUN LED is not turned ON after resetting a PLC CPU, there may be a hardware malfunction in the module. Please contact your local Mitsubishi service representative.

6.2.3 When the digital output value cannot be read

Items to Check	Corrective Actions
Is the RUN LED of A1S68AD flashing or turned OFF?	Follow the procedures in Section 6.2.1 or 6.2.2.
Is the ERROR LED of the CPU is turned ON?	Check the error description according to the CPU User's Manual.
Is the RUN LED of the CPU flashing or turned OFF?	Check the error description listed in the CPU User's Manual.
Is the condition to execute FROM instruction turned ON?	Monitor with a peripheral device such as GPP, and confirm whether it is ON or OFF.
Does the address of the buffer memory specified with a FROM instruction correspond to the address of the digital output value of channel to be read?	Check the sequence program.
Is the channel specified with FROM instruction set to A-D conversion enable?	Read the buffer memory address 0 and verify whether it is set to conversion enable or disable.
Is the conversion completed in the channel specified with FROM instruction ?	Read the buffer memory address 28 and verify the conversion completed flag.
Is the analog input signal line disconnected, or does an error occur?	Confirm the error by visually checking or conduction checking the signal line.
Disconnect the analog input wire of A1S68AD, and apply the test voltage (Stabilized power supply or battery) to the terminal of this module to measure the digital output value.	If the digital output value is normal with an A1S68AD, the module is affected by noise with external wiring. Therefore, check the wiring and grounding method.

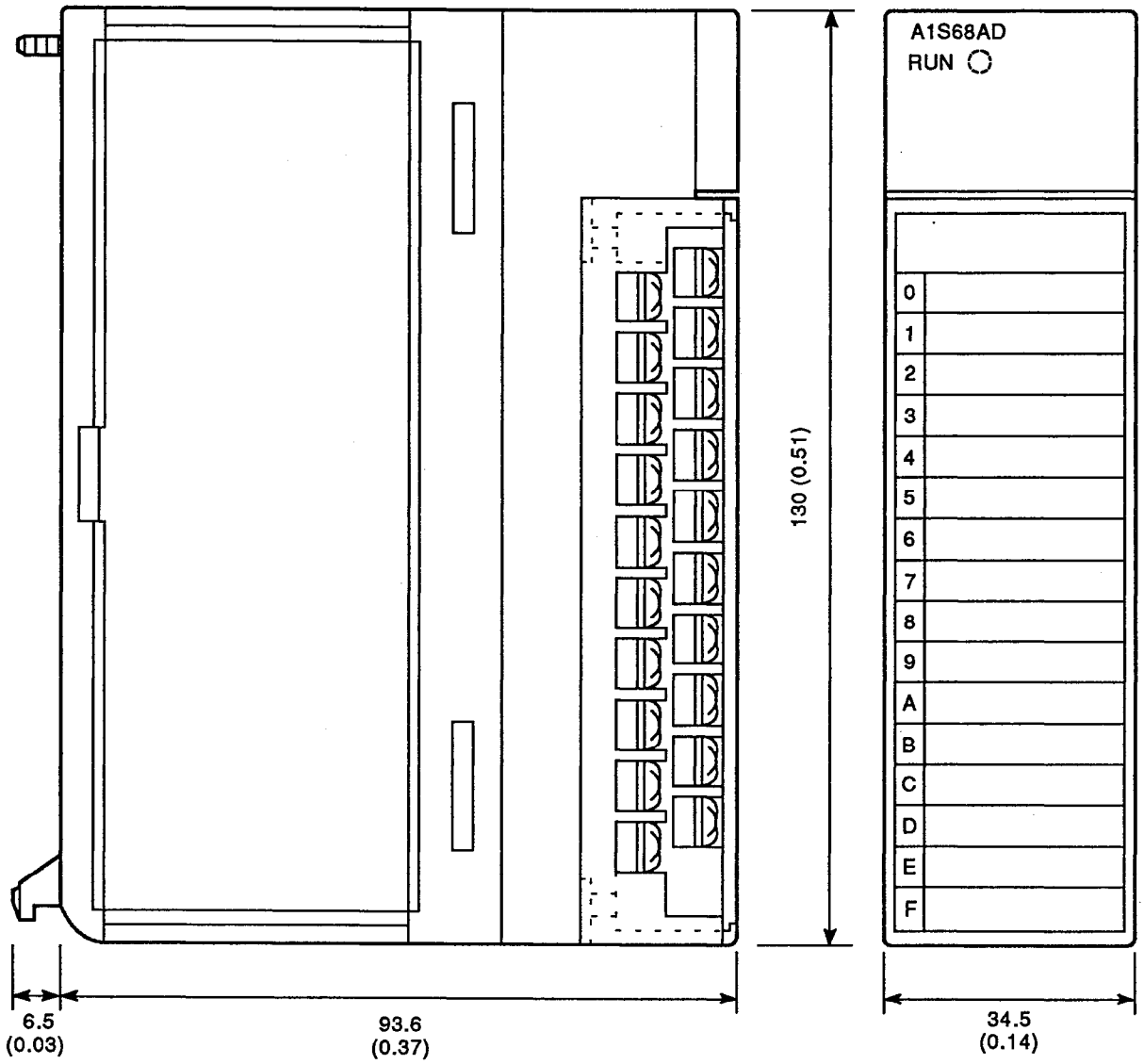
APPENDICES

APPENDIX 1 COMPARISON OF PERFORMACE BETWEEN A1S68AD AND A1S64AD

Table APP-1 Comparison of Performance

Item	Specification			
	A1S68AD		A1S64AD	
Analog input	Voltage: 0 to ±10 VDC(input resistance: 1MΩ) Current: 0 to +20 mA (input resistance: 250Ω) (Can be selected by setting DIP switches.)		Voltage: 0 to ±10 VDC(input resistance: 1 MΩ) Current: 0 to ±20 mA (input resistance: 250 Ω) (Can be selected by using inputterminals.)	
Digital output	16-bit signed binary			
I/O characteristic	Analog Input Value	Digital Output Value (Input Range: -10 to 10 V)	Analog Input Value	Digital Output Value Gain: 5 VResolution: 1/12000
	+10V +5V 0V -5V -10V	+2000 +1000 0 -1000 -2000	+10V +5V 0V -5V -10V	+12000 +6000 0 -6000 -12000
Maximum resolution	Voltage input	1 mV (input range: 1 to 5 V)	Voltage input	0.83 mV (resolution: 1/12000)
	Current input	4 μA (input range: 4 to 20 mA)	Current input	3.33 μA (resolution: 1/12000)
Overall accuracy	Within ±1% (accuracy with respect to the maximum value)			
Maximum conversion speed	0.5 ms/channel		20 ms/channel	
Absolute maximum input	Voltage	±35V	Voltage	±15V
	Current	±30mA	Current	±30mA
Number of analog I/O points	8 channels/module		4 channels/module	
Insulation method	Between input terminal and PC power supply: Photocoupler insulation (Between channels: No insulation)			
Number of occupied I/O points	Special 32 points			
Connection terminal	20-terminal block			
External power supply	Unnecessary			
Applicable wire size	0.75 to 1.5mm^2			
Applicable solderless terminal	R1.25-3, 1.25-YS3, RAV1.25-3, V1.25-YS3A			
Internal current consumption (5VDC)	0.31 A		0.4 A	
Weight kg(lb)	0.27 (0.59)		0.25 (0.55)	

APPENDIX 2 OUTSIDE DIMENSION



Unit:mm (inch)

WARRANTY

Please confirm the following product warranty details before starting use.

1. Gratis Warranty Term and Gratis Warranty Range

If any faults or defects (hereinafter "failure") found to be the responsibility of Mitsubishi occurs during use of the product within the gratis warranty term, the product shall be repaired at no cast via the dealer or Mitsubishi Service Company. Note that if repairs are required at a site overseas, on a detached island or remote place, expenses to dispatch an engineer shall be charged for.

[Gratis Warranty Term]

The gratis warranty term of the product shall be for one year after the date of purchase or delivery to a designated place.

Note that after manufacture and shipment from Mitsubishi, the maximum distribution period shall be six (6) months, and the longest gratis warranty term after manufacturing shall be eighteen (18) months. The gratis warranty term of repair parts shall not exceed the gratis warranty term before repairs.

[Gratis Warranty Range]

(1) The range shall be limited to normal use within the usage state, usage methods and usage environment, etc., which follow the conditions and precautiors, etc., given in the instruction manual, user's manual and caution labels on the product.

(2) Even within the gratis warranty term, repairs shall be charged for in the following cases.

1. Failure occurring from inappropriate storage or handling, carelessness or negligence by the user. Failure caused by the user's hardware or software design.
2. Failure caused by unapproved modifications, etc., to the product by the user.
3. When the Mitsubishi product is assembled into a user's device, failure that could have been avoided if functions or structures, judged as necessary in the legal safety measures the user's device is subject to or as necessary by industry standards, had been provided.
4. Failure that could have been avoided if consumable parts (battery, backlight, fuse, etc.) designated in the instruction manual had been correctly serviced or replaced.
5. Failure caused by external irresistible forces such as fires or abnormal voltages, and failure caused by force majeure such as earthquakes, lightning, wind and water damage.
6. Failure caused by reasons unpredictable by scientific technology standards at time of shipment from Mitsubishi.
7. Any other failure found to not be the responsibility of Mitsubishi or the user.

2. Onerous repair term after discontinuation of production

(1) Mitsubishi shall accept onerous product repairs for seven (7) years after production of the product is discontinued. Discontinuation of production shall be notified with Mitsubishi Technical Bulletins, etc.

(2) Product supply (including repair parts) is not possible after production is discontinued.

3. Overseas service

Overseas, repairs shall be accepted by Mitsubishi's local overseas FA Center. Note that the repair conditions at each FA Center may differ.

4. Exclusion of chance loss and secondary loss from warranty liability

Regardless of the gratis warranty term, Mitsubishi shall not be liable for compensation to damages caused by any cause found not to be the responsibility of Mitsubishi, chance losses, lost profits incurred to the user by failures in Mitsubishi products, damages and secondary damages caused from special reasons regardless of Mitsubishi's expectations, compensation for accidents, and compensation for damages to products other than Mitsubishi products and other duties.

5. Changes in product specifications

The specifications given in the catalogs, manuals or technical documents are subject to chage without prior notice.

6. Product application

(1) In using the Mitsubishi MELSEC programmable logic controller, the usage conditions shall be that the application will not lead to a major accident even if any problem or fault should occur in the programmable logic controller device, and that backup and fail-safe functions are systematically provided outside of the device for any problem or fault.

(2) The Mitsubishi general-purpose programmable logic controller has been designed and manufactured for applications in general industries, etc. Thus, applications in which the public could be affected such as in nuclear power plants and other power plants operated by respective power companies, and applications in which a special quality assurance system is required, such as for each Japan Railways company or the Department of Defense shall be excluded from the programmable logic controller applications.

Note that even with these applications, if the user approves that the application is to be limited and a special quality is not required, application shall be possible.

When considering use in aircraft, medical applications, railways, incineration and fuel devices, manned transport devices, equipment for recreation and amusement, and safety devices, in which human life or assets could be greatly affected and for which a particularly high reliability is required fin terms of safety and control system, please consult with Mitsubishi and discuss the required specifications.

A/D converter module type A1S68AD

User's Manual

MODEL	A1S68AD-U-E
MODEL CODE	13J757
IB(NA)-66576-B(0104)MEE	

MITSUBISHI ELECTRIC CORPORATION

HEAD OFFICE : MITSUBISHI DENKI BLDG MARUNOUCHI TOKYO 100-8310 TELEX : J24532 CABLE MELCO TOKYO
NAGOYA WORKS : 1-14 , YADA-MINAMI 5 , HIGASHI-KU, NAGOYA , JAPAN

When exported from Japan, this manual does not require application to the Ministry of Economy, Trade and Industry for service transaction permission.

Specifications subject to change without notice.